L Number	Hits	Search Text	DB	Time stamp
16	11	vid[$4	USPAT	2004/09/02 16:39
17	8	fid[$4	USPAT	2004/09/02 16:35
19	1824	vid voltage adj (id identif$4 identification)	USPAT	2004/09/02 16:44
20	6119	fid frequency adj (id identif$4 identification)	USPAT	2004/09/02 16:45
21	8	(vid voltage adj (id identif$4 identification)) same (fid frequency adj (id identif$4 identification))	USPAT	2004/09/02 16:47
25	9	vid[$4	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:44
26	3	fid[$4	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:44
27	959	vid voltage adj (id identif$4 identification)	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:45
28	4254	fid frequency adj (id identif$4 identification)	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:45
29	5	(vid voltage adj (id identif$4 identification)) same (fid frequency adj (id identif$4 identification))	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:45
30	17	(vid voltage adj (id identif$4 identification)) and (fid frequency adj (id identif$4 identification))	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:45
31	28	(vid voltage adj (id identif$4 identification)) and (fid frequency adj (id identif$4 identification))	USPAT	2004/09/02 16:48
32	347	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same (ic "integrated circuit")	USPAT	2004/09/02 16:53
34	49	32.ab.	USPAT	2004/09/02 17:02
35	57	32.clm.	USPAT	2004/09/02 16:51
36	14	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same ("north bridge" northbridge "system controller")	USPAT	2004/09/02 16:58
37	2	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same ("memory controller hub" mch chipset)	USPAT	2004/09/02 17:00
38	8	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same ("north bridge" northbridge "system controller")	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 16:58
40	3	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same ("memory controller hub" mch chipset)	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:00
41	3	(overclock$3 over adj clock$3) adj (chip ic controller interface "integrated circuit")	USPAT	2004/09/02 17:13
42	1039	overclock$3 over adj clock$3	USPAT	2004/09/02 17:18
43	43	42.ab.	USPAT	2004/09/02 17:10
44	104	42.clm.	USPAT	2004/09/02 17:10
45	71	(overclock$3 over adj clock$3) same (bridge hub controller)	USPAT	2004/09/02 17:11
51	69	overclock$3 over adj clocking	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:29
52	104	overclock$3 over adj clocking	USPAT	2004/09/02 17:23
53	17	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same overrid$3	USPAT	2004/09/02 17:27
54	969	(voltage frequency) near4 overrid$3	USPAT	2004/09/02 17:27
55	52	54.ab.	USPAT	2004/09/02 17:26
56	208	54.clm.	USPAT	2004/09/02 17:26
57	73	override near2 table	USPAT	2004/09/02 17:31

58	8	((vid voltage adj (id identif$4 identification)) (fid frequency adj (id identif$4 identification))) same overrid$3	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:27
59	192	(voltage frequency) near4 overrid$3	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:27
60	43	59.ab.	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:28
61	36	59.clm.	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:28
62	51	override near2 table	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:28
63	0	(overclock$3 over adj clocking) adj circuit	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:29
64	6	(overclock$3 over adj clocking) near2 circuit	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:29
67	819	override adj (circuit chip controller ic "integrated circuit")	USPAT	2004/09/02 17:31
68	150	override adj (chip controller ic "integrated circuit")	USPAT	2004/09/02 17:32
69	33	override adj (chip controller ic "integrated circuit")	US-PGPUB; EPO; JPO; IBM_TDB	2004/09/02 17:32